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		Subclass
	Class	
ISSUE CLASSIFICATION		

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED JK4 Q.A. Su

APPLICATION NO. 09/927011	CONT/PRIOR	CLASS 324 314	SUBCLASS 718	ART UNIT 2000 2133	EXAMINER Decady
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L. Farnsworth
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Edward Horton

Internal cache for on chip test data storage

TIME

PTO-2040
12/09

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
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None **Other**

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